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B. Hilliard

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Nally et al.

Serial No.: 08/376,919

Filed: January 23, 1995

Art Unit: 2415

Examiner: U. Chauhan

For: APPARATUS, SYSTEMS AND METHODS FOR CONTROLLING GRAPHIC AND VIDEO DATA IN MULTIMEDIA DATA PROCESSING AND DISPLAY SYSTEMS

Assistant Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

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Pursuant to Applicant's duty, under 37 C.F.R. § 1.56, §1.97 and §1.98, to disclose all relevant material, the Applicant lists below the following reference believed to be relevant to the subject matter of the above-identified application:

	Patent No.	Issue Date	Inventor
1.	5,406,306	04/11/95	Siann et al.

Reference 1. Patent No. 5,406,306 issued to Siann et al., entitled "SYSTEM AND METHOD OF DISPLAYING INFORMATION FROM A GRAPHICS MEMORY AND A VIDEO MEMORY ON A DISPLAY MONITOR," ("'306 patent") discloses an apparatus for, and methods of, displaying a graphics color image and digital video color

image on a video monitor, the video color image being displayed in a window on the video monitor.

The '306 patent is directed to the same general problem as is the invention disclosed in the above-identified patent application but deals with the problem in a different manner. In the '306 patent there is also a single memory for storing both graphics and video data. However, in the '306 patent the data is divided according to graphics or video with each being stored in a different portion of the memory. The data is then treated separately throughout the circuit. For example, the video data is processed at one frequency and the graphics data is processed at a different frequency. It is only when the two different data forms are actually sent to the monitor are they both processed at the same frequency. The '306 specification primarily deals with the concept of handling the processing of the different data forms (video and graphic) using different frequencies and does not, in any manner, deal with or even hint at the concept of dividing the data into on-screen and off-screen portions. Neither does the '306 patent deal with or even hint at 1) dividing the single memory into on-screen and off-screen portions, or 2) providing the on-screen and off-screen portions to the screen under control of different pipelines, as is specifically claimed in the invention in the above-identified application.

Applicant respectfully submits that the invention in the above-identified application is patentably distinguishable over the cited reference known to Applicant and disclosed in the above Information Disclosure Statement.

Respectfully submitted,

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